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Patent

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Mukesh Patel

Application No.: 09/687,777

Filed: October 13, 2000

For: JAVA HARDWARE ACCELERATOR
USING MICROCODE ENGINE

) Group Art Unit: unknown

) Examiner: unknown

#6/A
pre audit
6-13-01
DC

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please enter the within amendments prior to examination of the above-referenced application.

In the Claims:

Please cancel Claims 10-15 and 35-36.

Please amend Claims 1 and 58 to read as follows:

1. (Amended) A system comprising:

a pipelined central processing unit with associated native program counter; and a hardware accelerator operably connected to the central processing unit, the hardware accelerator adapted to convert stack-based instructions into register-based instructions native to the central processing unit, the hardware accelerator including a reissue buffer, the reissue buffer adapted to store converted native instructions issued to the CPU along with an indication of the order of the instructions, the system is such that when the CPU returns from an interrupt, the reissue buffer examines the indication to determine whether to reissue a stored native instruction value.